

# ER-TFTM035-6

## TFT LCD Module Datasheet



## EastRising Technology Co., Limited

**Attention:**

- A. Some specifications of IC are not listed in this datasheet. Please refer to the IC datasheet for more details.
- B. The related documents for interfacing, demo code, ic datasheet are all available, please download from our web.
- C. Please pay more attention to "INSPECTION CRITERIA" in this datasheet. We assume you already agree with these criterions when you place an order with us. No more recommendations.

REV	DESCRIPTION	RELEASE DATE
1.0	Preliminary Release	Jan-09-2016

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## 1. ORDERING INFORMATION

### 1.1 Order Number

Part Number(Order Number)	Description
ER-TFTM035-6	3.5 inch TFT LCD Display Module with Breakout Board.
ER-DBTM035-6	8051 Microcontroller Development Board & Kit for ER-TFTM035-6

### 1.2 Display Image

ER-TFTM035-6 with Optional Resistive Touch Panel



ER-TFTM035-6 with Resistive Touch Panel ↑

ER-TFTM035-6 with Optional Capacitive Touch Panel



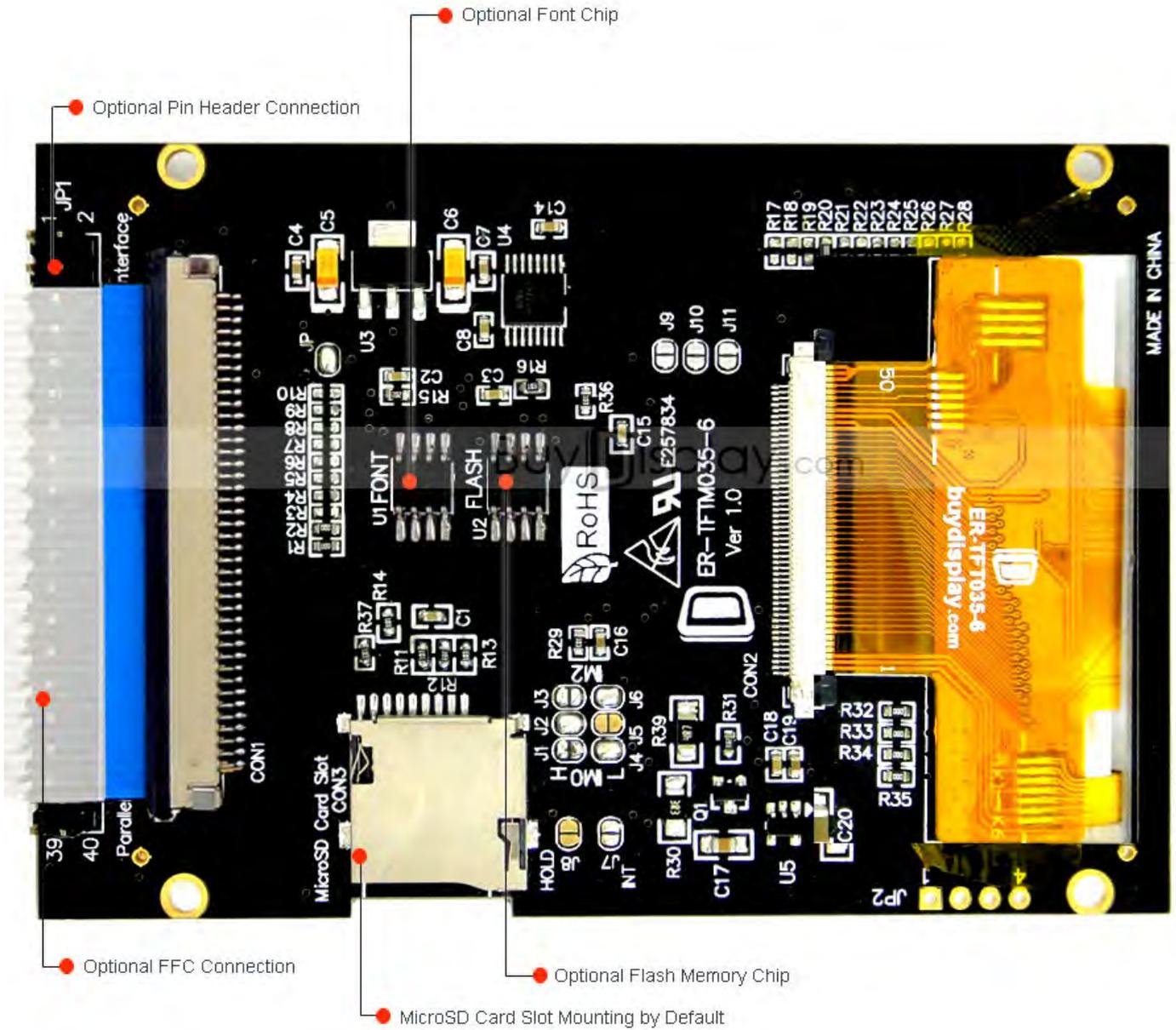
ER-TFTM035-6 with Capacitive Touch Panel ↑

ER-TFTM035-6 with No Touch Panel



ER-TFTM035-6 with No Touch Panel ↑

1.3 Breakout Board Image



## 2. SPECIFICATION

### 2.1 Display Specification

ITEM	STANDARD VALUE	UNIT
Display Format	320 (RGB) x 480 Dots	--
Display Connector	FFC or Pin Header	--
FPC Connector	40 Pins, 1.0mm Pitch, SMD Horizontal Type Top contact	--
Operating Temperature	-20 ~ +70	°C
Storage Temperature	-30 ~ +80	°C
Touch Panel Optional	Yes	--
Sunlight Readable	No	--

### 2.2 Mechanical Specification

ITEM	STANDARD VALUE	UNIT
Diagonal Size	3.5	inch
Outline Dimension(PCB)	66.4 × 96.7	mm
Active Area	49.96 × 73.44	mm
Dot Pitch	0.153 × 0.177	mm
Gross Weight	0.08	kg

### 2.3 Electrical Specification

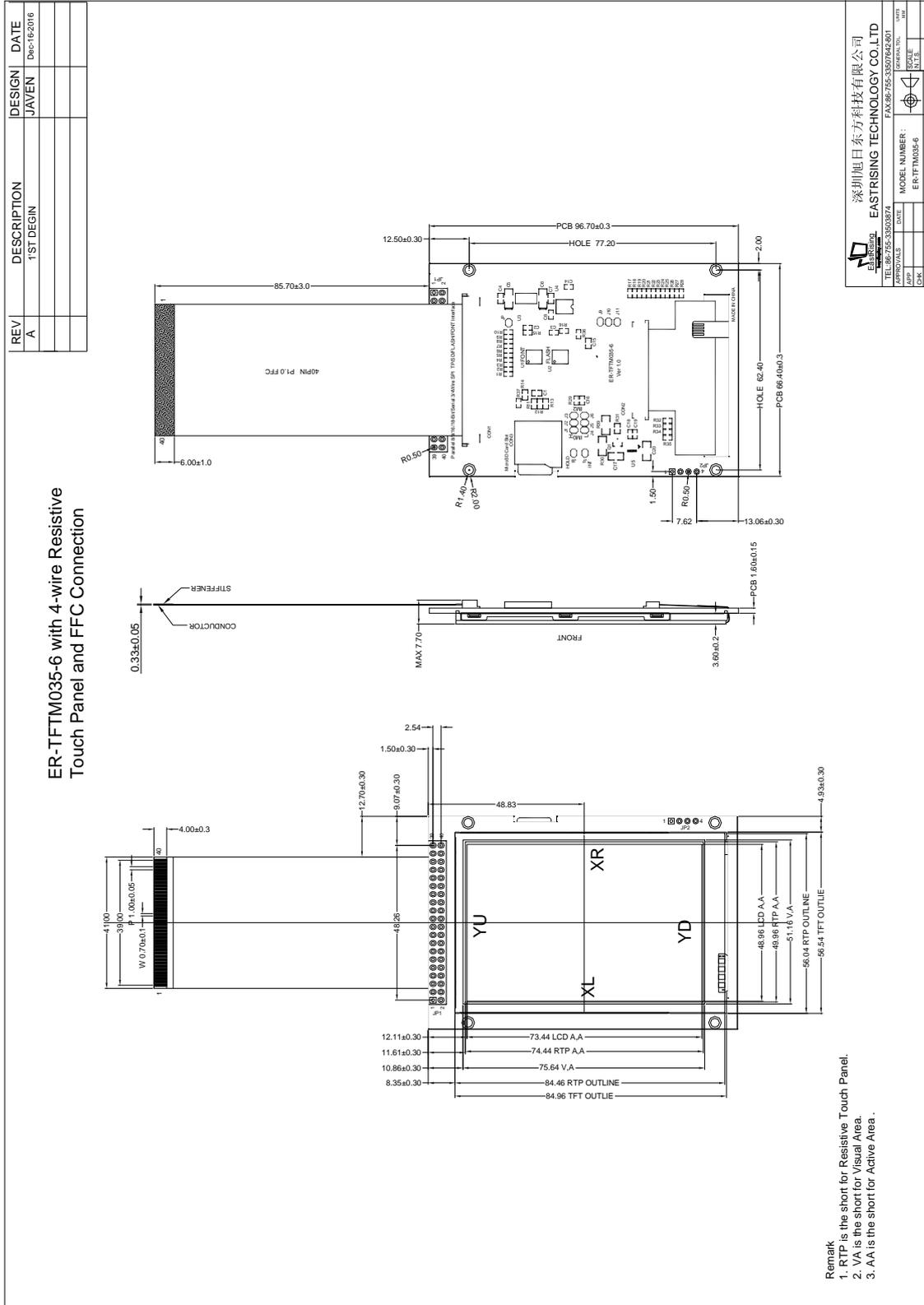
ITEM	STANDARD VALUE	UNIT
IC Package	SMT+COG	--
Controller	ILI9488	--
Interface	8080 8-bit Parallel , 8080 9-bit Parallel , 8080 16-bit Parallel , 8080 18-bit Parallel , 3-Wire Serial SPI, 4-Wire Serial SPI, RGB	--
Response Time (Typ)	20	ms

### 2.4 Optical Specification

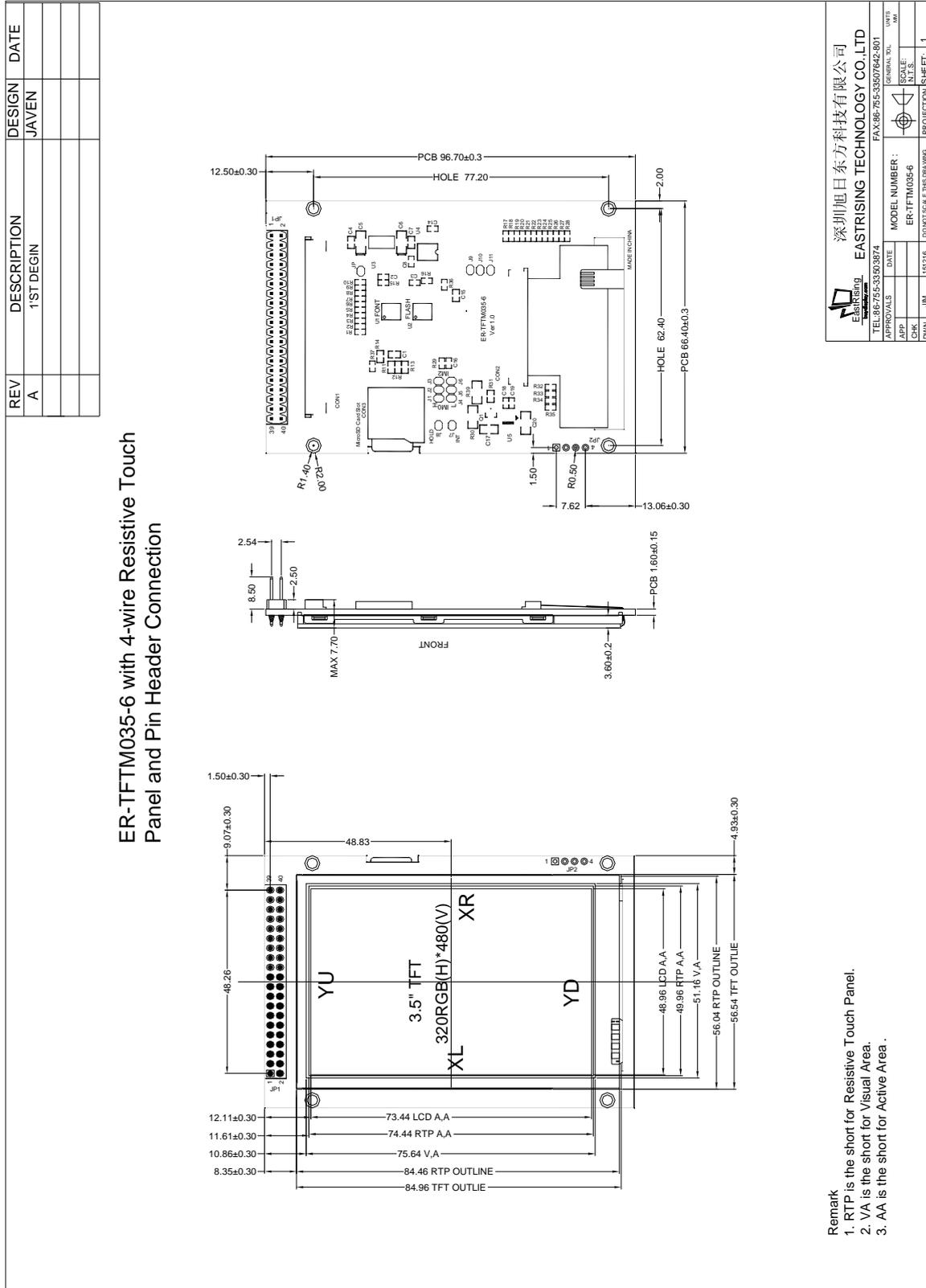
ITEM	STANDARD VALUE	UNIT
LCD Type	TFT-LCD / Transmissive / TN	--
Viewing Angle Range	Left:70, Right:70, Up:70, Down:50	deg
Colors	65K/262K	--
Contrast Ratio (Typ)	500:1	--
Brightness (Typ)	280	cd/m2

### 3. OUTLINE DRAWING

#### 3.1 Outline Drawing (FFC Connection & Resistive Touch Panel)



3.2 Outline Drawing (Pin Header Connection & Resistive Touch Panel)

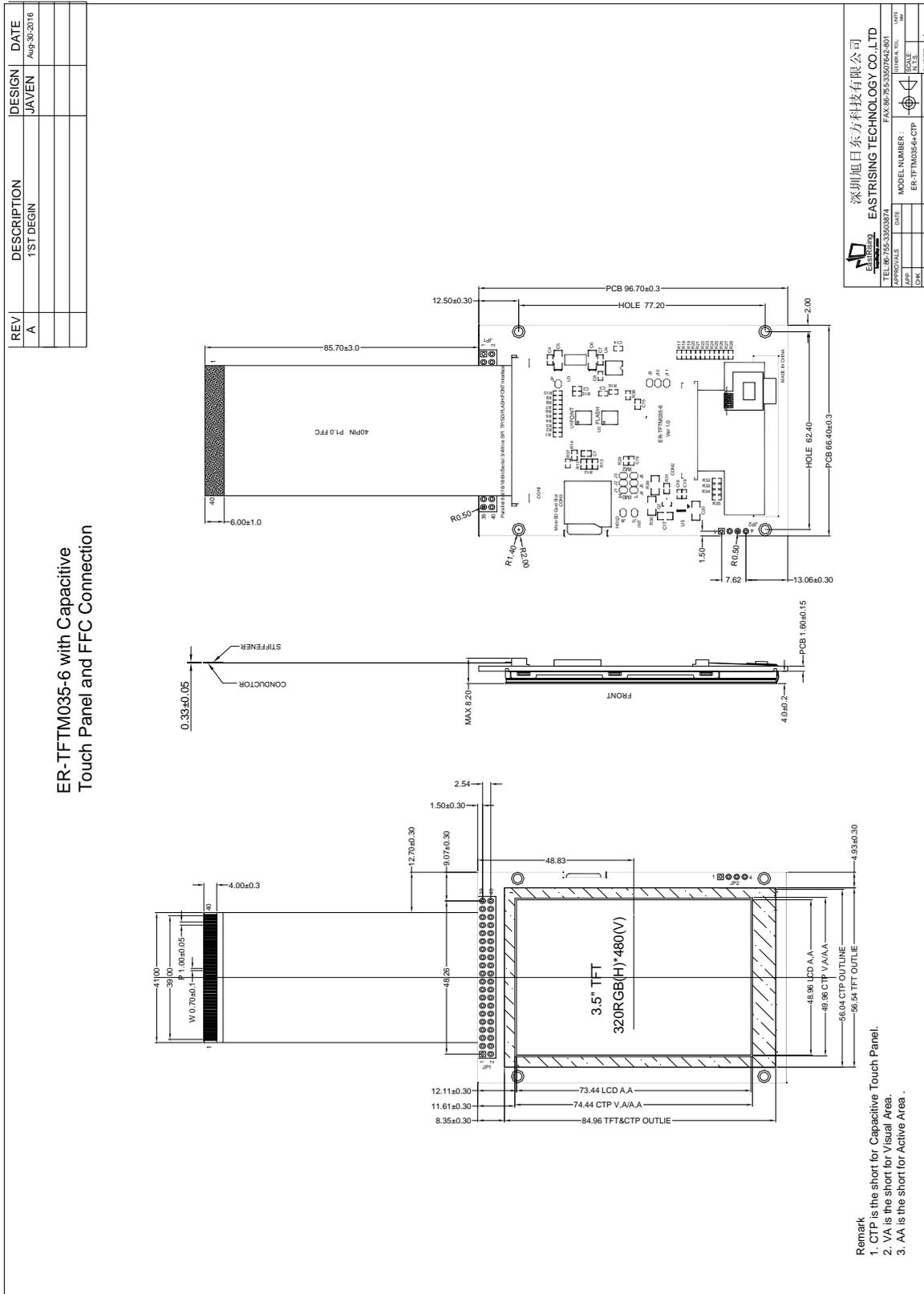


 深圳旭日东方科技有限公司 EASTRISING TECHNOLOGY CO. LTD	
TELEPHONE: 86-755-33593974	FAX: 86-755-33597642-801
APPROVALS:	DATE:
APP: [Signature]	DATE: [Blank]
DESIGN: [Signature]	DATE: [Blank]
DATE: 10/21/16	DO NOT SCALE THIS DRAWING
MODEL NUMBER: ER-TFTM035-6	PROJECTION: SHEET: 1

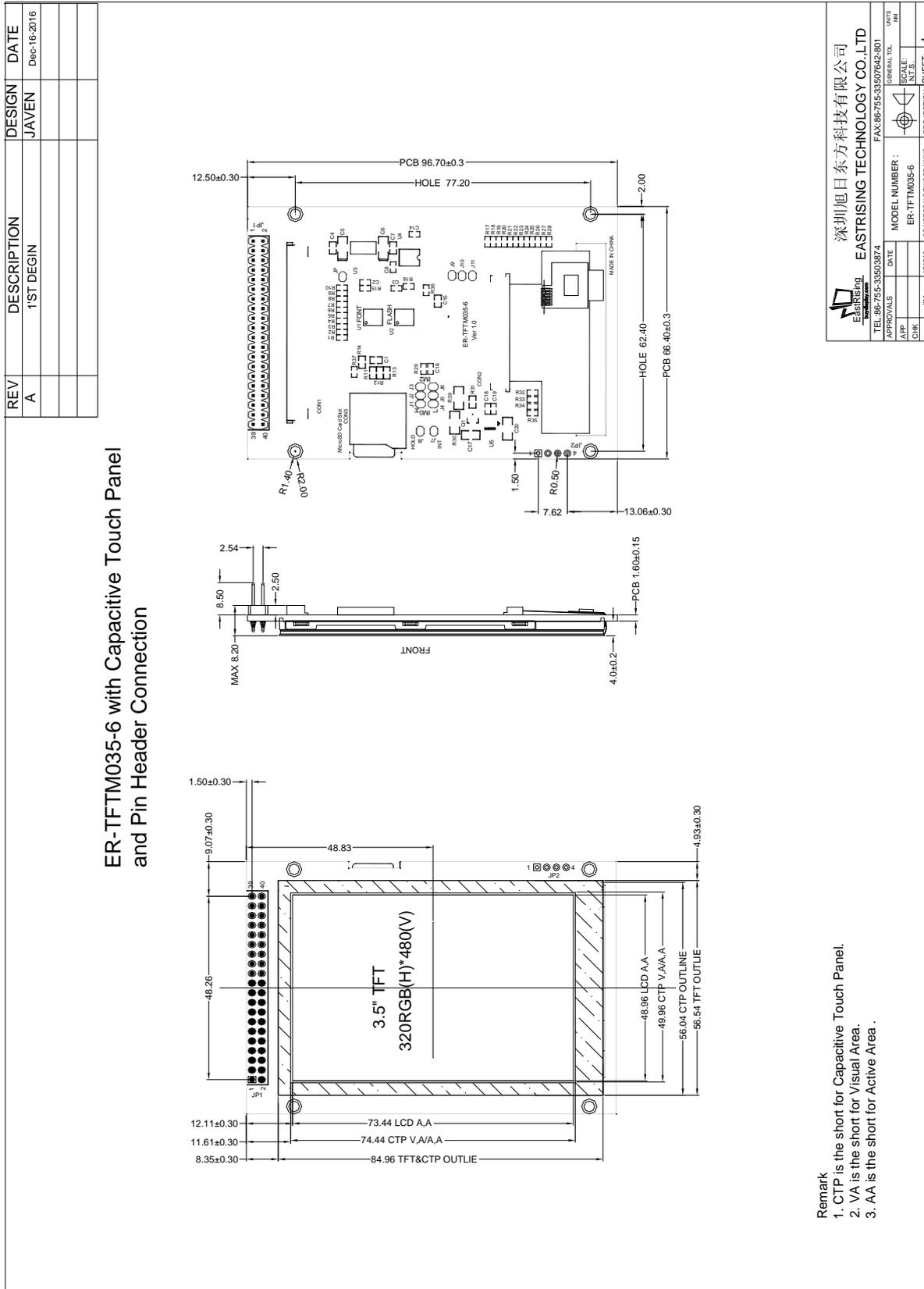
Remark

1. RTP is the short for Resistive Touch Panel.
2. VA is the short for Visual Area.
3. AA is the short for Active Area.

3.3 Outline Drawing (FFC Connection & Capacitive Touch Panel)



3.4 Outline Drawing (Pin Header Connection & Capacitive Touch Panel)



深圳旭日东方科技有限公司  
EASTRISING TECHNOLOGY CO.,LTD

TEL:86-755-33503874 FAX:86-755-3307842-801

APPROVALS	DATE	UNIT
APP		SCALE
CHK		INT.S.
DWN	JIM	151216

MODEL NUMBER: ER-TFTM035-6

PROJECTION SHEET: 1

## 4. ELECTRICAL SPEC

### 4.1 Pin Configuration-JP1/CON1 (Parallel/Serial Interface)

Pin No	Symbol	Descriptions
1	VSS	Ground
2	VDD	Power Supply
3-20	DB0-DB17	18-Bit Parallel Bi-Directional Data Bus. No connection when not in use
21	/RESET_NC	Master synchronizes reset, Active Low. RC reset on board. No connection when not in use
22	TE	Tearing effect output pin to synchronize MPU to frame writing. No connection when not in use
23	LCD_/CS	LCD chip select input pin ("Low" enable).
24	/WR(SCL)	Parallel interface: Write execution control pin ; SCL pin as Serial Clock when operates in the serial interface Fix to IOVCC or DGND level when not in use.
25	D/C	Data/Command Selection pin.Fix to IOVCC or DGND level when not in use.
26	/RD	Parallel interface: Serves as a read signal and MCU read data at the rising edge. No connection when not in use
27	LCD_SDI	serial interface I: Serial in/out signal. serial interface II: High, Serial input signal. The data is applied on the rising edge of the SCL signal. No connection when not in use
28	LCD_SDO	Serial output signal. The data is outputted on the falling edge of the SCL signal. No connection when not in use
29	BL_ON/OFF	Backlight control input. Low: OFF High: ON
30	RTP_/CS	Resistive touch screen : chip select input pin ("Low" enable).
	CTP_SCL	Capacitive touch screen : Serial colock input (I2C)
31	/RTP PEN	Resistive touch screen : pen interrupt when low.
	CTP_SDA	Capacitive touch screen : Serial data input/output (I2C)
32	SDO	Serial data output. (MicroSD card ,Touch,Falsh Memory, Font ) Shared pin
33	SCL	Serial clock signal input.(MicroSD card ,Touch,Falsh Memory, Font) Shared pin
34	SDI	Serial data input. (MicroSD card ,Touch,Falsh memory,Font ) Shared pin
35	SD_/CS	SD card chip select input pin ("Low" enable).

36	FONT_/CS	Font chip select input pin ("Low" enable).
37	FLASH_/CS	Flash memory chip select input pin ("Low" enable).
38	FLASH_/WP	Flash memory write Protect Input. Low active.
39	FLASH_/HOLD	Flash hold Input. Low active. J8 Short and J7 Open.
	CTP_INT	Capacitive touch screen the interrupt signal. Low active. J7 Short and J8 Open.
40	VSS	Ground

Remark: RTP is the short of Resistive Touch Panel

CTP is the short of Capacitive Touch Panel

#### 4.2 Pin Configuration-JP2 (RGB Interface)

Pin No	Symbol	Descriptions
1	VSYNC	Frame synchronizing signal for RGB interface operation. No connection when not in use
2	HSYNC	Line synchronizing signal for RGB interface operation. No connection when not in use
3	DOTCLK	Dot clock signal for RGB interface operation. No connection when not in use
4	DE	Data enable signal for RGB interface operation. No connection when not in use

4.3 Jump Point Description

Function Description	Jump Method
Power Supply Switch	JP Open: 5V VDD Supply / JP Short: 3.3V VDD Supply.
8080 8-bit Interface	J1,J2,J6 Short and J3,J4,J5 Open. R1~R10=0R,R20=0R and R17~R19,R21~R28 Not Soldering.
8080 9-bit Interface	J1,J5,J6 Short and J2,J3,J4 Open. R1~R9=0R,R20=0R and R10,R17~R19,R21~R28 Not Soldering.
8080 16-bit Interface	J2,J4,J6 Short and J1,J3,J5 Open. R1~R2=0R,R20=0R and R3~R10,R17~R19,R21~R28 Not Soldering.
8080 18-bit Interface	J4,J5,J6 Short and J1,J2,J3 Open. R20=0R and R1~R10,R17~R19,R21~R28 Not Soldering.
3-wire SPI Interface	J1,J3,J5 Short and J2,J4,J6 Open. R1-R10=0R, R17=0R,R19=0R,R21-R28=0R and R18,R20 not soldering.
4-wire SPI Interface	J1,J2,J3 Short and J4,J5,J6 Open. R1-R10=0R,R19=0R,R21-R28=0R and R17,R18,R20 not soldering.
18-bit RGB Interface	J1,J3,J5 Short and J2,J4,J6 Open. R17=0R ,R19=0R and R1~R10,R18,R20-R28,R32~R35 not soldering.
Capacitive Touch Panel	J7,J9,J10,J11 Short and J8 Open.
Resistive Touch Panel	J7,J9,J10,J11 Open
Flash hold Input	J8 Short and J7 Open

4.4 Absolute Maximum Ratings

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
Power Supply Voltage	VDD	-0.5	-	+5.5	V
Logic signal voltage	VDDIO	-0.5	-	+3.6	V
operating temperature	Top	-20	-	+70	° C
storage temperature	TST	-30	-	+80	° C
Humidity	RH	-		90%(Max60° C)	RH

4.5 Electrical Characteristics

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
Power Supply Voltage	VDD	--	3.3	3.6	V
		4.8	5.0	5.2	V
Logic Signal I/O Voltage	VDDIO	3.0	3.3	3.6	V
Input Voltage 'H' Level	VIH	0.8VDDIO	-	VDDIO	V
Input Voltage 'L' Level	VIL	VSS	-	0.3VDDIO	V
Output Voltage 'H' Level	VOH	0.8VDDIO	-	VDDIO	V
Output Voltage 'L' Level	VCL	VSS	-	0.2VDDIO	V
Module Current	IDD(3.3V)	--	115	130	mA
	IDD(5.0V)	--	130	150	mA

## 5. INSPECTION CRITERIA

### 5.1 Acceptable Quality Level

Each lot should satisfy the quality level defined as follows

<b>PARTITION</b>	<b>AQL</b>	<b>DEFINITION</b>
A. Major	0.4%	Functional defective as product
B. Minor	1.5%	Satisfy all functions as product but not satisfy cosmetic standard

### 5.2 Definition of Lot

One lot means the delivery quantity to customer at one time.

### 5.3 Condition of Cosmetic Inspection

#### ◆ INSPECTION AND TEST

- FUNCTION TEST
- APPEARANCE INSPECTION
- PACKING SPECIFICATION

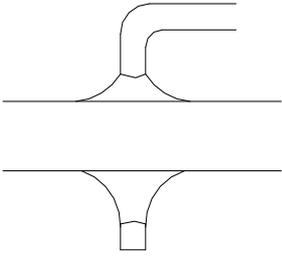
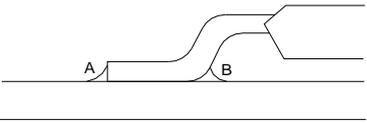
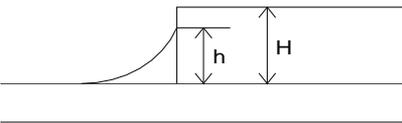
#### ◆ INSPECTION CONDITION

- Put under the lamp (20W) at a distance 100mm from
- Tilt upright 45 degree by the front (back) to inspect LCD appearance.

#### ◆ AQL INSPECTION LEVEL

- SAMPLING METHOD: MIL-STD-105D
- SAMPLING PLAN: SINGLE
- MAJOR DEFECT: 0.65% (MAJOR)
- MINOR DEFECT: 2.5% (MINOR)
- GENERAL LEVEL: II/NORMAL

5.4 Module Cosmetic Criteria

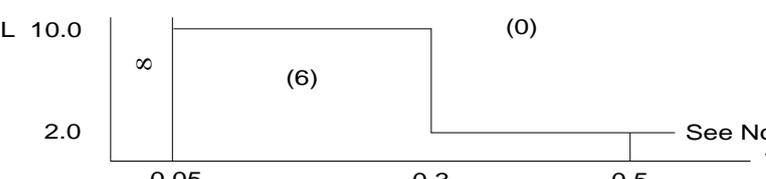
NO.	Item	Judgment Criterion	Partition
1	Difference in Spec.	None allowed	Major
2	Pattern Peeling	No substrate pattern peeling and floating	Major
3	Soldering defects	No soldering missing	Major
		No soldering bridge	Major
		No cold soldering	Minor
4	Resist flaw on substrate	Invisible copper foil( $\varnothing$ 0.5mm or more)on substrate pattern	Minor
5	Accretion of metallic Foreign matter	No soldering dust	Minor
		No accretion of metallic foreign matters(Not exceed $\varnothing$ 0.2mm)	
6	Stain	No stain to spoil cosmetic badly	Minor
7	Plate discoloring	No plate fading,rusting and discoloring	Minor
8	Solder amount	<p>a. Soldering side of PCB</p>  <p>Solder to form a 'Filet' all around the lead. Solder should not hide the lead form perfectly.(too much)</p> <p>b.Components side (In case of 'Through Hole PCB')</p> <p>Solder to reach the Components side of PCB</p>	Minor
	2.Flat packages	<p>Either 'toe'(A) or 'heel' (B) of the lead to be covered by 'Filet'</p>  <p>Lead form to be assume over Solder.</p>	Minor
	3.Chips	<p><math>(3/2) H \cong h \cong (1/2)H</math></p> 	Minor

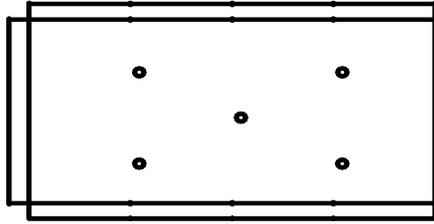
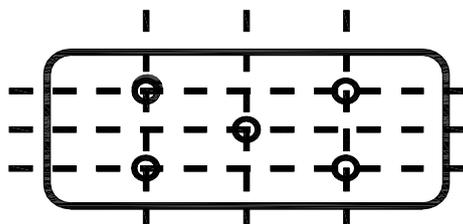
9	Backlight defects	<ol style="list-style-type: none"> <li>1. Light fails or flickers. (Major)</li> <li>2. Color and luminance do not correspond to specifications. (Major)</li> <li>3. Exceeds standards for display's blemishes, foreign matter, dark lines or scratches. (Minor)</li> </ol>	<p>See list ←</p>
10	PCB defects	<ol style="list-style-type: none"> <li>1. Oxidation or contamination on connectors.*</li> <li>2. Wrong parts, missing parts, or parts not in specification.*</li> <li>3. Jumpers set incorrectly. (Minor)</li> <li>4. Solder (if any) on bezel, LED pad, zebra pad, or screw hole pad is not smooth. (Minor)</li> </ol> <p>*Minor if display functions correctly. Major if the display fails.</p>	<p>See list ←</p>
11	Soldering defects	<ol style="list-style-type: none"> <li>1. Unmelted solder paste.</li> <li>2. Cold solder joints, missing solder connections, or oxidation.*</li> <li>3. Solder bridges causing short circuits.*</li> <li>4. Residue or solder balls.</li> <li>5. Solder flux is black or brown.</li> </ol> <p>*Minor if display functions correctly. Major if the display fails.</p>	<p>Minor</p>

5.5 Screen Cosmetic Criteria (Non-Operating)

No.	Defect	Judgment Criterion	Partition	
1	Spots	In accordance with Screen Cosmetic Criteria (Operating) No.1.	Minor	
2	Lines	In accordance with Screen Cosmetic Criteria (Operation) No.2.	Minor	
3	Bubbles in Polarizer		Minor	
		Size: d mm		Acceptable Qty in active area
		d ≤ 0.3		Disregard
		0.3 < d ≤ 1.0		3
1.0 < d ≤ 1.5	1			
1.5 < d	0			
4	Scratch	In accordance with spots and lines operating cosmetic criteria, When the light reflects on the panel surface, the scratches are not to be remarkable.	Minor	
5	Allowable density	Above defects should be separated more than 30mm each other.	Minor	
6	Coloration	Not to be noticeable coloration in the viewing area of the LCD panels. Back-lit type should be judged with back-lit on state only.	Minor	
7	Contamination	Not to be noticeable.	Minor	

5.6 Screen Cosmetic Criteria (Operating)

No.	Defect	Judgment Criterion	Partition	
1	Spots	A) Clear	Minor	
		Size:d mm		Acceptable Qty in active area
		$d \leq 0.1$		Disregard
		$0.1 < d \leq 0.2$		6
		$0.2 < d \leq 0.3$		2
		$0.3 < d$		0
Note: Including pin holes and defective dots which must be within one pixel Size.				
2	Lines	B) Unclear	Minor	
		Size:d mm		Acceptable Qty in active area
		$d \leq 0.2$		Disregard
		$0.2 < d \leq 0.5$		6
		$0.5 < d \leq 0.7$		2
		$0.7 < d$		0
Note: ( ) – Acceptable Qty in active area L - Length (mm) W -Width(mm) $\infty$ -Disregard				
A) Clear				
				
				
<p>'Clear' = The shade and size are not changed by Vo. 'Unclear' = The shade and size are changed by Vo.</p>				

No.	Defect	Judgment Criterion	Partition
3	Rubbing line	Not to be noticeable.	
4	Allowable density	Above defects should be separated more than 10mm each other.	Minor
5	Rainbow	Not to be noticeable.	Minor
6	Dot size	To be 95%~105%of the dot size (Typ.) in drawing. Partial defects of each dot (ex.pin-hole) should be treated as'spot'. (see Screen Cosmetic Criteria (Operating) No.1)	Minor
7	Brightness (only back-lit Module)	Brightness Uniformity must be $B_{MAX}/B_{MIN} \leq 2$ - BMAX :Max.value by measure in 5 points - BMIN : Min.value by measure in 5 points Divide active area into 4 vertically and horizontally. Measure 5 points shown in the following figure. 	Minor
8	Contrast Uniformity	Contrast Uniformity must be $B_{mMAX}/B_{mMIN} \leq 2$ Measure 5 points shown in the following figure. Dashed lines divide active area into 4 vertically and horizontally. Measuring points are located at the inter-sections of dashed line.  Note: BMAX – Max.value by measure in 5 points. BMIN – Min.value by measure in 5 points. O – Measuring points in $\varnothing 10\text{mm}$ .	Minor
<p>Note:</p> <p>(1) Size : <math>d=(\text{long length} + \text{short length})/2</math></p> <p>(2) The limit samples for each item have priority.</p> <p>(3) Complexed defects are defined item by item, but if the number of defects is defined in above table, the total number should not exceed 10.</p> <p>(4) In case of 'concentration', even the spots or the lines of 'disregarded' size should not be allowed. Following three situations Should be treated as 'concentration'. -7 or over defects in circle of <math>\varnothing 5\text{mm}</math>.</p>			

-10 or over defects in circle of  $\varnothing$  10mm  
-20 or over defects in circle of  $\varnothing$  20mm

## 6. PRECAUTIONS FOR USING

### 6.1 Handling Precautions

- ◆ This device is susceptible to Electro-Static Discharge (ESD) damage. Observe Anti-Static precautions.
- ◆ EastRising display panel is made of glass. Do not subject it to a mechanical shock by dropping it or impact.
- ◆ If EastRising display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- ◆ Do not apply excessive force to the EastRising display surface or the adjoining areas since this may cause the color tone to vary.
- ◆ The polarizer covering the EastRising display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- ◆ If EastRising display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following Isopropyl or alcohol.
- ◆ Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the Water.
- ◆ Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
- ◆ Install the EastRising LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the cable or the backlight cable.
- ◆ Do not attempt to disassemble or process EastRising LCD module.
- ◆ NC terminal should be open. Do not connect anything.
- ◆ If the logic circuit power is off, do not apply the input signals.
- ◆ To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
  - Be sure to ground the body when handling EastRising LCD modules.
  - Tools required for assembling, such as soldering irons, must be properly grounded.
  - To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions.
  - The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

### 6.2 Power Supply Precautions

- ◆ Identify and, at all times, observe absolute maximum ratings for both logic and LC drivers. Note that there is some variance between models.
- ◆ Prevent the application of reverse polarity to VDD and VSS, however briefly.
- ◆ Use a clean power source free from transients. Power-up conditions are occasionally jolting and may exceed the maximum ratings of EastRising modules.
- ◆ The VDD power of EastRising module should also supply the power to all devices that may access the display. Don't allow the data bus to be driven when the logic supply to the module is turned off.

### 6.3 Operating Precautions

- ◆ DO NOT plug or unplug EastRising module when the system is powered up.
- ◆ Minimize the cable length between EastRising module and host MPU.
- ◆ For models with backlights, do not disable the backlight by interrupting the HV line. Unload inverters produce voltage extremes that may arc within a cable or at the display.
- ◆ Operate EastRising module within the limits of the modules temperature specifications.

### 6.4 Mechanical/Environmental Precautions

- ◆ Improper soldering is the major cause of module difficulty. Use of flux cleaner is not recommended as they may seep under the electrometric connection and cause display failure.
- ◆ Mount EastRising module so that it is free from torque and mechanical stress.
- ◆ Surface of the LCD panel should not be touched or scratched. The display front surface is an easily scratched, plastic polarizer. Avoid contact and clean only when necessary with soft, absorbent cotton dampened with petroleum benzene.
- ◆ Always employ anti-static procedure while handling EastRising module.
- ◆ Prevent moisture build-up upon the module and observe the environmental constraints for storage temperature.
- ◆ Do not store in direct sunlight
- ◆ If leakage of the liquid crystal material should occur, avoid contact with this material, particularly ingestion. If the body or clothing becomes contaminated by the liquid crystal material, wash thoroughly with water and soap

### 6.5 Storage Precautions

When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps. Keep EastRising modules in bags (avoid high temperature / high humidity and low temperatures below 0C). Whenever possible, EastRising LCD modules should be stored in the same conditions in which they were shipped from our company.

### 6.6 Others

Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.

If EastRising LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also

appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.

To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.

- Exposed area of the printed circuit board.
- Terminal electrode sections.

## **7. USING LCD MODULES**

### **7.1 Liquid Crystal Display Modules**

EastRising LCD is composed of glass and polarizer. Pay attention to the following items when handling.

- ◆ Please keep the temperature within specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.
- ◆ Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.).
- ◆ N-hexane is recommended for cleaning the adhesives used to attach front/rear polarizers and reflectors made of organic substances which will be damaged by chemicals such as acetone, toluene, ethanol and isopropylalcohol.
- ◆ When EastRising display surface becomes dusty, wipe gently with absorbent cotton or other soft material like chamois soaked in petroleum benzin. Do not scrub hard to avoid damaging the display surface.
- ◆ Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading.
- ◆ Avoid contacting oil and fats.
- ◆ Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizers. After products are tested at low temperature they must be warmed up in a container before coming in contact with room temperature air.
- ◆ Do not put or attach anything on EastRising display area to avoid leaving marks on.
- ◆ Do not touch the display with bare hands. This will stain the display area and degrade insulation between terminals (some cosmetics are determined to the polarizers).
- ◆ As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring.

### **7.2 Installing LCD Modules**

- ◆ Cover the surface with a transparent protective plate to protect the polarizer and LC cell.
- ◆ When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be  $\pm 0.1$ mm.

### 7.3 Precaution for Handling LCD Modules

Since EastRising LCM has been assembled and adjusted with a high degree of precision; avoid applying excessive shocks to the module or making any alterations or modifications to it.

- ◆ Do not alter, modify or change the shape of the tab on the metal frame.
- ◆ Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
- ◆ Do not damage or modify the pattern writing on the printed circuit board.
- ◆ Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
- ◆ Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
- ◆ Do not drop, bend or twist EastRising LCM.

### 7.4 Electro-Static Discharge Control

Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC.

- ◆ Make certain that you are grounded when handling LCM.
- ◆ Before remove LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential.
- ◆ When soldering the terminal of LCM, make certain the AC power source for the soldering iron does not leak.
- ◆ When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.
- ◆ As far as possible make the electric potential of your work clothes and that of the work bench the ground potential.
- ◆ To reduce the generation of static electricity be careful that the air in the work is not too dried. A relative humidity of 50%-60% is recommended.

### 7.5 Precaution for Soldering to EastRising LCM

- ◆ Observe the following when soldering lead wire, connector cable and etc. to the LCM.

-Soldering iron temperature :  $280^{\circ}\text{C} \pm 10^{\circ}\text{C}$

-Soldering time: 3-4 sec.

-Solder: eutectic solder.

If soldering flux is used, be sure to remove any remaining flux after finishing to soldering operation. (This does not apply in the case of a non-halogen type of flux.) It is recommended that you protect the LCD surface with a cover during soldering to prevent any damage due to flux spatters.

- ◆ When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.
- ◆ When remove the electroluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.

#### 7.6 Precaution for Operation

- ◆ Viewing angle varies with the change of liquid crystal driving voltage (VO). Adjust VO to show the best contrast.
- ◆ Driving the EastRising LCD in the voltage above the limit shortens its life.
- ◆ Response time is greatly delayed at temperature below the operating temperature range. However, this does not mean the LCD will be out of the order. It will recover when it returns to the specified temperature range.
- ◆ If EastRising display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.
- ◆ Condensation on terminals can cause an electrochemical reaction disrupting the terminal circuit. Therefore, it must be used under the relative condition of 40°C, 50% RH.
- ◆ When turning the power on, input each signal after the positive/negative voltage becomes stable.

#### 7.7 Limited Warranty

Unless agreed between EastRising and customer, EastRising will replace or repair any of its LCD modules which are found to be functionally defective when inspected in accordance with EastRising LCD acceptance standards (copies available upon request) for a period of one year from date of shipments. Cosmetic/visual defects must be returned to EastRising within 90 days of shipment. Confirmation of such date shall be based on freight documents. The warranty liability of EastRising limited to repair and/or replacement on the terms set forth above. EastRising will not be responsible for any subsequent or consequential events.

#### 7.8 Return Policy

No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are:

- Broken LCD glass.
- PCB eyelet damaged or modified.
- PCB conductors damaged.
- Circuit modified in any way, including addition of components.
- PCB tampered with by grinding, engraving or painting varnish.
- Soldering to or modifying the bezel in any manner.

Module repairs will be invoiced to the customer upon mutual agreement. Modules must be returned with sufficient description of the failures or defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB eyelet's, conductors and terminals

## **8. IMAGE STICKING**

### **8.1 What is Image Sticking**

If you remain a fixed image on TFT LCD Display for a long period of time, you may experience a phenomenon called Image Sticking. Image Sticking - sometimes also called “image retention” or “ghosting” - is a phenomenon where a faint outline of a previously displayed image remains visible on the screen when the image is changed. It can occur at variable levels of intensity depending on the specific image makeup, as well as the amount of time the core image elements are allowed to remain unchanged on the screen. In POS applications, for example, a button menu which remains fixed, or in which the “frame” elements (core image) remain fixed and the buttons may change, may be susceptible to image sticking. It is important to note that if the screen is used exclusively for this application, the user may never notice this phenomenon since the screen never displays other content. It is only when an image other than the “retained” image is shown on the screen that this issue becomes evident. Image sticking is different than the “burn-in” effect commonly associated with phosphor based devices.

### **8.2 What causes Image Sticking**

Image sticking is an intrinsic behavior of TFT LCD displays due to the susceptibility to polarization of the interior materials (liquid crystals) when used under static, charged conditions (continuously displaying the same image). The individual liquid crystals in an LCD panel have unique electrical properties. Displaying a fixed pattern - such as the POS menu described above – over prolonged periods can cause a parasitic charge build-up (polarization) within the liquid crystals which affects the crystals’ optical properties and ultimately prevents the liquid crystal from returning to its normal, relaxed state when the pattern is finally changed. This effect takes place at a cellular level within the LCD, and the effect can cause charged crystal alignment at the bottom or top of a crystal cell in the “z” axis, or even crystal migration to the edges of a cell, again based on their polarity. These conditions can cause image sticking over an entire area, or at boundaries of distinct color change respectively. In either case, when the liquid crystals in the pixels and sub-pixels utilized to display the static image are polarized such that they can not return fully to their “relaxed” state upon deactivation, the result is a faint, visible, retained image on the panel upon presentation of a new, different image. The actual rate of image retention depends on variation factors such as the specific image, how long it is displayed unchanged, the temperature within the panel and even the specific panel brand due to manufacturing differences amongst panel manufacturers.

### 8.3 How to Avoid Image Sticking?

- Try not to operate the LCD with a “fixed” image on the screen for more than 2 hours.
- If you are operating the monitor in an elevated temperature environment and with a displayed image which is contrary to the recommendations in “For Software Developers” below, image stick can occur in as little as 30 minutes. Adjust your screen saver settings accordingly.
- Power down the unit during prolonged periods of inactivity such as the hours a store is closed or a shift during which the piece of equipment isn’t used.
- Use a screensaver with a black or medium gray background that is automatically set to come on if the device is inactive for more than 5-10 minutes.
- Avoid placing the monitor in poorly ventilated areas or in areas that will create excess heat around the monitor. For Software Developers.
- In defining the icons, buttons, or windows in the screen, try to utilize block patterns instead of distinct lines as borders for dividing the display into distinct areas.
- If it is necessary to display a static image, try to use colors that are symmetric to the middle grey level at the boundary of two different colors, and slightly shift the borders line once in a while.
- Try to utilize medium gray hues for those areas that will have prolonged display times or remain static as other menu elements change.

### 8.4 How to Avoid Image Sticking?

Unlike the usually irreversible “burn-in” effects commonly associated with direct view phosphor display devices such as CRTs, an image retained on an LCD display can be reversed – often to a point of total invisibility. However, the severity of the underlying causes (as described above) of the image retained on a specific display, as well as the variation factors (see “For Software Developers” above) under which the retained image was created, will dictate the final level of retention reversal. One way to erase a retained image on a panel is to run the screen (monitor “on”) in an “all black” pattern for 4-6 hours. It is also helpful to do this in an elevated temperature environment of approximately 35° to 50°C. Again, utilizing a dynamic screen saver with an all black background during prolonged idle display periods is a good way to avoid image retention issues.

### 8.5 Is Image Sticking Covered by EastRising RMA Warranty ?

Image sticking is a phenomenon inherent to TFT LCD display technology itself, and as such, the occurrence of this “ghosting” effect is considered normal operation by the manufacturers of the LCD display modules which are integrated into today’s monitor solutions. EastRising does not warrant any display against the occurrence of image sticking. We strongly advise that you follow the operating recommendations listed above to avoid the occurrence of this phenomenon.

**That’s the end of the datasheet**